Agenda Sun Application Tuning Seminar

July 7-9, 2009

University of Coimbra Physics Department Rua Larga Coimbra, Portugal

Part I - Tuning Serial Applications

Tuesday	July	7	09:00	-	17:30	
Wednesday	July	8	09:00	-	12:30	
Part II - Shared Memory Parallelization						
Wednesday	July	8	14:00	-	17:30	
Thursday	July	9	09:00	-	17:30	

Instructor

Roland Rambau, Principal Field Technologist GSE Platform Technology Team Sun Microsystems Germany

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Lunch break

12:30-14:00

Part I - Tuning Serial Applications

Γι	uesday July 7 - Morning Session			
	Welcome and Introduction	09:00-09:30		
	Prologue	09:30-10:00		
	The Memory Hierarchy	10:00-10:30		
	Coffee/tea break	10:30-10:45		
	Processor Architectures The Sun Compilers - Part I	10:45-11:30 11:30-12:30		
	Lunch break	12:30-14:00		
T	uesday July 7 - Afternoon Session			
	Lab 1 - Getting started and Compiler Options Review Lab 1	14:00-14:45 14:45-15:00		
	Coffee/tea break	15:00-15:15		
	The Sun Compilers - Part II	15:15-16:30		
	Demo - Performance Analyzer	16:30-16:45		
	The Sun Performance Analyzer	16:45-17:30		
Wednesday July 8 - Morning Session				
	Review of first day and demos	09:00-09:15		
	Serial Optimization Techniques	09:15-10:30		
	Coffee/tea break	10:30-10:45		
	Performance Tuning Case Studies	10:45-11:30		
	Lab 2 - Performance Analyzer Review Lab 2	11:30-12:15 12:15-12:30		

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17:00-17:30

Part II - Shared Memory Parallelization

Wednesday July 8 - Afternoon Session	
Introduction into Parallelization - Part I	14:00-15:00
Coffee/tea break	15:00-15:15
Introduction into Parallelization - Part II	15:15-15:45
Automatic Parallelization	15:45-16:15
Lab 3 - Automatic Parallelization Review Lab 3	16:15-16:45 16:45-17:00
Overview of OpenMP – Part I	17:00-17:30
Thursday July 9 - Morning Session	
Review of second day and demos	09:00-09:15
Overview of OpenMP – Part II	09:15-10:30
Coffee/tea break	10:30-10:45
Lab 4 - Parallelization with OpenMP, Part I	10:45-12:30
Lunch break	12:30-14:00
Thursday July 9 - Afternoon Session	
Lab 4 - Parallelization with OpenMP, Part II	14:00-15:00
Coffee/tea break	15:00-15:15
Overview of OpenMP – Part III	15:15-15:45
OpenMP and Performance - Part I	15:45-16:15
Lab 4 - Parallelization with OpenMP, Part III Lab 5 - OpenMP performance tuning (for more experienced users)	16:15-16:45
Review Lab 4 and Lab 5	16:45-17:00

Close Of Seminar

OpenMP and Performance - Part II

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